

REMARKS

Claims 1, 15, 17 and 24 have been amended, and new claims 26-47 added. Claim 26 substantially corresponds to claim 1, and claims 27-47 correspond to claims 3-23. The claim amendments have been made for improved clarity, and not for reasons related to patentability. Support for the amendments may be found, for example, in Fig. 3 and the corresponding disclosure. No new matter has been added. (Reference to the specification and drawings is for exemplary purposes only, and not intended to limit the scope of the invention).

Applicant acknowledges with appreciation the interview courteously afforded to them on June 23, 2011. The telephone interview was conducted between Examiner Richard Torrente and Applicant's representative, Kevin R. Spivak (Reg. No. 43,148). During the interview, claim 1 and the Yamada reference we discussed. Specifically, it was discussed that in the instant claimed invention all data from each processing block is exchanged via the dedicated data buses through the memory interface. The Examiner agreed that amendment of the claims in this regard would overcome the rejection of record.

Claim 15 has been objected to in the Office Action due to informalities. Claim 15 has been amended accordingly.

Claims 1 and 3-25 have been rejected under 35 USC 112, second paragraph, due to lack of antecedent basis. Claim 17 has been amended accordingly. Applicant is unable to determine the antecedent basis problem in claim 1. The Examiner is kindly requested to elaborate on the rejection.

Claims 1, 3-5, 9, 11-21, 24 and 25 have been rejected under 35 USC 103(a) as unpatentable over Owen (US 7,321,368) in view of Yamada (US 2002/0023237). Claims 6-8 have been rejected under 35 USC 103(a) as unpatentable over Owen in view of Yamada, further

in view of O'Sullivan (US 2005/0235134). Claim 10 has been rejected under 35 USC 103(a) as unpatentable over Owen in view of Yamada, further in view of Yamazaki (US 2004/0079952). Claims 22 and 23 have been rejected under 35 USC 103(a) as unpatentable over Owen in view of Yamada, further in view of Smith (US 2005/0053028). The rejections are respectfully traversed.

The Examiner cites Owen as disclosing the features of the instant invention, with the exception of each of the respective plural data buses being dedicated data buses. However, Yamada is cited for this feature. Specifically, the Examiner states that "Yamada...discloses an integrated circuit setup wherein each processing block (e.g. see 401 in fig. 9) is connected to a memory interface (see 402 in fig. 9) by each of respective plural dedicated data buses (see fig. 9)." As discussed during the interview, Applicant respectfully disagrees.

The instant claimed invention (for example, amended claims 1 and 24, and new claim 26) requires that when data is transferred between any two of the microcomputer, stream input/output, media process and TV input/output, all transferred data is exchanged through the memory interface (or all data exchanged therebetween is through the memory interface). As discussed and agreed upon during the interview, in Yamada, there is no disclosure of all transferred data between the various blocks being exchanged through the memory interface.

Since the recited structure and method are not disclosed by the applied prior art, either alone or in combination, amended claims 1 and 24 are allowable. New claim 26, substantially corresponding to amended claim 1, is similarly allowable. All other claims depend directly or indirectly from one of independent claims 1, 24 or 26 and are similarly allowable. None of the other cited and applied prior art remedies any of the deficiencies of Owen or Yamada.

In view of the fact that none of the art of record, whether considered alone or in combination, discloses or suggests the present invention as now defined by the pending claims, and

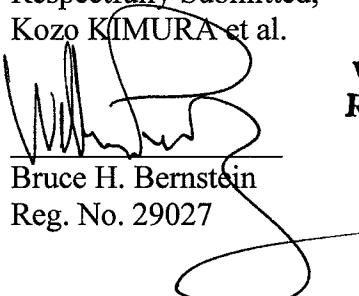
in further view of the above amendments and remarks, reconsideration of the Examiner's action and allowance of the present application are respectfully requested and are believed to be appropriate.

Should the Commissioner determine that an extension of time is required in order to render this response timely and/or complete, a formal request for an extension of time, under 37 C.F.R. §1.136(a), is herewith made in an amount equal to the time period required to render this response timely and/or complete. The Commissioner is authorized to charge any required extension of time fee under 37 C.F.R. §1.17 to Deposit Account No. 19-0089.

If there should be any questions concerning this application, the Examiner is invited to contact the undersigned at the telephone number listed below.

Respectfully Submitted,
Kozo KIMURA et al.

Bruce H. Bernstein
Reg. No. 29027


William Pieprz
Reg. No. 33,630

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GREENBLUM & BERNSTEIN, P.L.C.
1950 Roland Clarke Place
Reston, VA 20191
(703) 716-1191